

ABSTRACT OF DISCLOSURE

*Sub A2*

In a digital-signal-processing circuit comprising an LUT memory used for storing a gamma-correction LUT used for carrying out gamma correction on an input digital video signal and a front-stage signal-processing unit provided at a stage preceding the LUT memory and used for carrying signal processing such as contrast adjustment, after the input digital video signal having a typical width of 8 bits is subjected to signal processing such as contrast adjustment in the signal-processing unit to be transformed into a digital video signal with a width of 11 bits, the 11-bit digital video signal is supplied to the LUT memory having an output bit count of 10 bits, which is smaller than the 11-bit width of the digital video signal supplied to the LUT memory but greater than the 8-bit width of the digital video signal input to the signal-processing unit.

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